













**REGULAR EXAMINATION RESULT OF SEMESTER - III (ELECTRONICS & TELECOMMUNICATION) EXAMINATION HELD IN NOV-DEC 2018 (REVISED COURSE)**

Seat No	A.R.E	Male	Name of Candidate	Sessional	Term Work	Theory	Theory	Total	Total	Practical	Practical	Oral	Passes	Total	Total	Remarks	PR Number
Roll No	Code	Female		Marks	Marks								Fails	Sem-3			
Category				50/25	50/25	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace				Grace		
<b>183347</b>	<b>A</b>	<b>F</b>	<b>NAIK DIVYA UTTAM</b>														<b>201704602</b>
<b>171104042</b>																	
<b>OBC</b>			Applied Mathematics - III	17		70		87					P				
			Economics and Management	15		60		75					P				
			Algorithms for Data Structures	18	23	80		121					P				
			Electronic Devices and Circuits - I	21		65		86		22			P				
			Digital System Design	14		66		80		19			P				
			Electrical Circuits and Systems	25		74		99				21	P				
							<b>Sem-2 PASS</b>		<b>Sem-1 PASS</b>					<b>610</b>		<b>PASSES</b>	
<b>183348</b>	<b>A</b>	<b>M</b>	<b>NAIR SHRIHARI SABU</b>														<b>201704598</b>
<b>171104043</b>																	
<b>GENERAL</b>			Applied Mathematics - III	21		78		99					P				
			Economics and Management	19		79		98					P				
			Algorithms for Data Structures	19	25	92		136					P				
			Electronic Devices and Circuits - I	21		68		89		23			P				
			Digital System Design	20		73		93		24			P				
			Electrical Circuits and Systems	19		80		99				20	P				
							<b>Sem-2 PASS</b>		<b>Sem-1 PASS</b>					<b>681</b>		<b>PASSES</b>	
<b>183349</b>	<b>A</b>	<b>M</b>	<b>NAYAK SHINKRE OMKAR DEOKINANDAN</b>														<b>201704596</b>
<b>171104044</b>																	
<b>GENERAL</b>			Applied Mathematics - III	24		87		111					P				
			Economics and Management	11		52		63					P				
			Algorithms for Data Structures	18	25	88		131					P				
			Electronic Devices and Circuits - I	20		66		86		22			P				
			Digital System Design	18		66		84		25			P				
			Electrical Circuits and Systems	22		79		101				20	P				
							<b>Sem-2 PASS</b>		<b>Sem-1 PASS</b>					<b>643</b>		<b>PASSES</b>	
<b>183350</b>	<b>A</b>	<b>F</b>	<b>NEHA MURTHY</b>														<b>201705160</b>
<b>171104045</b>																	
<b>CSP</b>			Applied Mathematics - III	12		50		62					P				
			Economics and Management	13		62		75					P				
			Algorithms for Data Structures	7	16	57		80					P				
			Electronic Devices and Circuits - I	15		54		69		23			P				
			Digital System Design	10		40		50		19			P				
			Electrical Circuits and Systems	17		35		52		\$5		21	P				
							<b>Sem-2 PASS</b>		<b>Sem-1 PASS</b>					<b>451</b>	<b>\$5</b>	<b>PASSES</b>	
<b>183351</b>	<b>A</b>	<b>F</b>	<b>OLI SUSHANTI P</b>														<b>201711487</b>
<b>171104046</b>																	
<b>GENERAL</b>			Applied Mathematics - III	21		66		87					P				
			Economics and Management	12		52		64					P				
			Algorithms for Data Structures	17	15	71		103					P				
			Electronic Devices and Circuits - I	20		67		87		19			P				
			Digital System Design	15		48		63		23			P				
			Electrical Circuits and Systems	23		83		106				24	P				
							<b>Sem-2 PASS</b>		<b>Sem-1 PASS</b>					<b>576</b>		<b>PASSES</b>	
<b>183352</b>	<b>A</b>	<b>M</b>	<b>PEDNEKAR AMBAR SANJAY</b>														<b>201704574</b>
<b>171104049</b>																	
<b>GENERAL</b>			Applied Mathematics - III	17		62		79					P				
			Economics and Management	11		56		67					P				
			Algorithms for Data Structures	15	22	74		111					P				
			Electronic Devices and Circuits - I	14		51		65		23			P				
			Digital System Design	10		41		51		25			P				
			Electrical Circuits and Systems	17		55		72				19	P				
							<b>Sem-2 PASS</b>		<b>Sem-1 PASS</b>					<b>512</b>		<b>PASSES</b>	





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Roll No	Code	Female		Marks	Marks								Fails	Sem-3			
Category				50/25	50/25	Obtd.	Grace	Obtd.	Grace	Obtd.	Grace				Grace		
<b>183359</b>	<b>A</b>	<b>M</b>	<b>SINAI GAITONDE ADITYA DATTA</b>														<b>201704577</b>
<b>171104058</b>																	
<b>GENERAL</b>			Applied Mathematics - III	17		78		95					P				
			Economics and Management	20		69		89					P				
			Algorithms for Data Structures	21	25	71		117					P				
			Electronic Devices and Circuits - I	20		63		83		21			P				
			Digital System Design	17		50		67		25			P				
			Electrical Circuits and Systems	19		80		99				24	P				
							<b>Sem-2 PASS</b>		<b>Sem-1 PASS</b>					<b>620</b>		<b>PASSES</b>	
<b>183360</b>	<b>A</b>	<b>M</b>	<b>SUMEET LOHAR</b>														<b>201711490</b>
<b>171104059</b>																	
<b>GENERAL</b>			Applied Mathematics - III	18		72		90					P				
			Economics and Management	21		76		97					P				
			Algorithms for Data Structures	20	25	85		130					P				
			Electronic Devices and Circuits - I	23		77		100		23			P				
			Digital System Design	11		57		68		15			P				
			Electrical Circuits and Systems	18		45		63				20	P				
							<b>Sem-2 PASS</b>		<b>Sem-1 PASS</b>					<b>606</b>		<b>PASSES</b>	
<b>183361</b>	<b>A</b>	<b>M</b>	<b>TARI RASHMIT RAMESH</b>														<b>201704567</b>
<b>171104060</b>																	
<b>OBC</b>			Applied Mathematics - III	20		63		83					P				
			Economics and Management	19		58		77					P				
			Algorithms for Data Structures	25	25	82		132					P				
			Electronic Devices and Circuits - I	16		62		78		20			P				
			Digital System Design	15		42		57		25			P				
			Electrical Circuits and Systems	22		77		99				22	P				
							<b>Sem-2 PASS</b>		<b>Sem-1 PASS</b>					<b>593</b>		<b>PASSES</b>	
<b>183362</b>	<b>A</b>	<b>M</b>	<b>UDIPI KRISHNAPRASAD</b>														<b>201704584</b>
<b>171104061</b>																	
<b>GENERAL</b>			Applied Mathematics - III	21		71		92					P				
			Economics and Management	22		74		96					P				
			Algorithms for Data Structures	23	25	75		123					P				
			Electronic Devices and Circuits - I	17		60		77		24			P				
			Digital System Design	21		66		87		24			P				
			Electrical Circuits and Systems	23		78		101				23	P				
							<b>Sem-2 PASS</b>		<b>Sem-1 PASS</b>					<b>647</b>		<b>PASSES</b>	
<b>183363</b>	<b>A</b>	<b>F</b>	<b>VANPUZHAVAYALIL RINU MANOJ</b>														<b>201704581</b>
<b>171104062</b>																	
<b>GENERAL</b>			Applied Mathematics - III	21		65		86					P				
			Economics and Management	17		50		67					P				
			Algorithms for Data Structures	17	23	76		116					P				
			Electronic Devices and Circuits - I	16		60		76		20			P				
			Digital System Design	14		49		63		25			P				
			Electrical Circuits and Systems	23		56		79				19	P				
							<b>Sem-2 PASS</b>		<b>Sem-1 PASS</b>					<b>551</b>	<b>#10</b>	<b>PASSES</b>	
<b>183364</b>	<b>A</b>	<b>F</b>	<b>VELIP DHANASHREE SADANAND</b>														<b>201704578</b>
<b>171104063</b>																	
<b>ST</b>			Applied Mathematics - III	10		21		31					F				
			Economics and Management	11		40		51					P				
			Algorithms for Data Structures	6	11	67		84					P				
			Electronic Devices and Circuits - I	10		47		57		14			P				
			Digital System Design	7		44		51		10			P				
			Electrical Circuits and Systems	13		35	\$5	48	\$5			10	P				
							<b>Sem-2 FAIL</b>		<b>Sem-1 PASS</b>					<b>356</b>	<b>\$5</b>	<b>FAILS</b>	









